IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFF	In Application of:)	Examiner:	Auve, Glenn A.
, JAN 2 5	10 l)	Examiner.	riuve, Gleim ri.
A TRADE	Addication No.: 10/676 887)))	Art Unit:	2111
	Filed: September 30, 2003))		
	For: MECHANISM TO CONTROL HARDWARE INTERRUPT ACKNOWLEDGMENT IN A VIRTUAL MACHINE SYSTEM)))		
	Mail Stop Amendment			
	Commissioner for Patents			
	P.O. Box 1450			
	Alexandria, VA 22313-1450			
	INFORMATION DISCL	OSURES	TATEMENT	
	Sir:	<u>obert b</u>	TATELVILLIA	
	Enclosed is a copy of Information Disclo	sure Citati	on Form PTO-	1449 together with
	copies of the documents cited on that form. It is	respectful	ly requested th	at the cited
	documents be considered and that the enclosed of	opy of Inf	ormation Discl	osure Citation Form
	PTO-1449 be initialed by the Examiner to indica	ite such co	nsideration and	a copy thereof
	returned to applicant(s).			
	FIRST CLASS CERTIF	ICATE OF	MAILING	
	I hereby certify that this correspondence is being deposited with sufficient postage in an envelope addressed to the Co 22313-1450	d with the Ur mmissioner	nited States Postal for Patents, P.O. E	Service as first class mail sox 1450, Alexandria, VA
	on <u>January 22, 2007</u>			
	Date of Deposit			
	Michelle Begay Name of Person Mailing Corresponde			
	Name of Person Mailing Corresponde	nce		
_	Midell R_	January :	22, 2007	·
•	Signature		Date	

42P16348 1

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

37 C.	F.R. §1.97(b).
	F.R. §1.97(c). If so, then enclosed with this Information Disclosure ment is <u>one</u> of the following:
A stat	tement pursuant to 37 C.F.R. §1.97(e) or
A che	eck for \$180.00 for the fee under 37 C.F.R. § 1.17(p).
	F.R. §1.97(d). If so, then enclosed with this Information Disclosure ment are the following:
(1)	A statement pursuant to 37 C.F.R. §1.97(e);
(2)	A petition requesting consideration of the Information Disclosure Statement; and
	37 C.: Stater A state A che 37 C.: Stater (1)

(3) A check for \$_____ for the fee under 37 C.F.R. §1.17(i) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: January 22, 2007

Marina Portnova Reg. No. 45,750

12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026 (408) 720-8300

PTO/SB/08A(06-03)
Approved for use through 07/31/2008. CMB 0851-0231
US Patent & Tredemark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless II contains a valid OMB control number.

| Complete if Known | Complete if Known | Complete in the control number | Complete if Known | Complete in the control number | Complete in the c Substitute for form 1449A/PTO INFORMATION DISCLOSURE **Application Number** 10/676,887 STATEMENT BY APPLICANT September 30, 2003 **Filing Date** Steven M. Bennett, et al. **First Named Inventor** 2111 **Group Art Unit** JAN 2 5 2007 Auve, Glenn A. **Examiner Name** Attorney Docket No: 42P16348 Sheet 1 of 3

	US PATENT DOCUMENTS				
Examiner	USP Document	Publication Date	Name of Patentee or	Pages,Columns,Lines,Where Relevant Passages	
Initial *	Number		Applicant of cited Document	or Relevant Figures Appear	

		FOREIGN PATE	ENT DOCUMENTS		
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines, Where Relevant Passages or Relevant Figures Appear	T²
	DE-DE4217444	12/03/1992	Toyohisa, Imada , et al.	·	
	EP-EP0473913	03/11/1992	Farrell, Joel A.		
	EP-EP0600112	06/08/1994	Wimmer, Manfred D.		
	EP-EP0930567	07/21/1999	Brewer, Jason M.		
	EP-EP1030237	08/23/2000	Proudler, John , et al.		
	EP-EP1146715	10/17/2001	Wang, Xin , et al.		
	JP-JP2000076139	03/14/2000	Tanno, Masaaki , et al.	ŧ	
	WO-WO0062232	10/19/2000	Headrick, Samual P., et al.		
	WO-WO0127723	04/19/2001	Vamvakas, Athanasia , et al.		
	WO-WO0127821	04/19/2001	Chen, Liqun, et al.		
	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.		
>	WO-WO0175565	10/11/2001	Ellison, Carl M.		
	WO-WO0175595	10/11/2001	Ellison, Carl M., et al.		
	WO-WO02086684	10/31/2002	Proudler, Graeme J.		
	WO-WO0217555	02/28/2002	Ford, Warwick		
	WO-WO9729567	08/14/1997	Gressel, Carmi, et al.		
	WO-WO9834365	08/06/1998	Devanbu, Premkumar T., et al.		
	WO-WO9844402	10/08/1998	Bramhill, Ian D., et al.		
	WO-WO9905600	02/04/1999	Garst, Blaine, et al.		
	WO-WO9909482	01/15/2002	Bausch, Jean		
	WO-WO9957863	11/11/1999	Hayes Jr., Kent F., et al.		

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		BERG, CLIFF, "How Do I Create a Signed Applet?", <u>Dr. Dobb's Journal</u> , (08/1997),1-9	
		BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995), Chapter 3	
		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", 7th Annual IEEE Symposium, FCCM '99 Proceedings, XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA,	
		(4/21/1999),209-221 COMPAQ COMPUTER CORPORATION, "Trusted Computing Platform Alliance	

EXAMINER

DATE CONSIDERED

PTC/SB/084(06-03)
Approved for use through 07/31/2008, OMB 0651-0031
US Patent & Tradement Office: U.S. DEPARTMENT or COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known Substitute for form 1449A/PTO INFORMATION DISCLOSURE **Application Number** 10/676,887 STATEMENT BY APPLICANT September 30, 2003 **Filing Date** (Use as many sheets as necessary) First Named Inventor Steven M. Bennett, et al. **Group Art Unit** 2111 **Examiner Name** Auve, Glenn A. Attorney Docket No: 42P16348 Sheet 2 of 3

	OTHER	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		(TCPA) Main Specification Version 1.1a", XP002272822, (1/25/2001),1-321	
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through	
		Cryptographic Authentication", Proceedings of the Symposium on Security and	
	va	Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
		GOLDBERG, ROBERT P., "Survey of Virtual Machine Research", <u>COMPUTER</u> Magazine, (06/1974),34-35	
		GONG, LI, et al., "Going Behond the Sandbox: An Overview of the New Security	
1		Architecture in the Java Development Kit 1.2", Proceedings of the USENIX	
	*	Symposium on Internet Technologies and Systems, Monterey, CA,(12/1997),	i
		GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual	
		Machines", <u>IBM J. Research Development, Vol 27, Number 6,</u> (11/1983),530-544	
		HEINRICH, JOE, "MIPS R4000 Microprocessor User's Manual, Second	
		Edition", Chapter 4 "Memory Management", (6/11/1993),61-97	1
		IBM, "Information Display Technique for a Terminate Stay Resident Program	
		IBM Technical Disclosure Bulletin", TDB-ACC-No. NA9112156, Vol. 34, Issue	-
		<u>7A,</u> (12/1/1991),156-158	
		INTEL, "Intel386 DX Microprocessor 32-Bit CHMOS Microprocessor With	
		Integrated Memory Management", (1995),5-56	
		KARGER, PAUL A., et al., "A VMM Security Kernal for the VAX Architecture",	
		Proceedings of the Symposium on Research in Security and Privacy,	
		XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	
		KASHIWAGI, KAZUHIKO, et al., "Design and Implementation of Dynamically	
		Reconstructing System Software", Software Engineering Conference,	
		Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos,	
		CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996),	l
		LAWTON, KEVIN, et al., "Running Multiple Operating Systems Concurrently on	
		an IA32 PC Using Virtualization Techniques",	
		http://www.plex86.org/research/paper.txt, (11/29/1999),1-31	
		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers",	
		IEEE AES Systems Magazine, XP002190614,(March 1999),	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", CRC Press	
		LLC, USA XP002201307, (1997),475	
	41.	MOTOROLA, "M68040 User's Manual", (1993),1-1 to 8-32	
		RICHT, STEFAN, et al., "In-Circuit-Emulator Wird Echtzeittauglich", Elektronic,	
		Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-	
		103),8-6-1991	
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure	
		Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium,	
		XP002247347, Denver, Colorado, (8/14/00),1-17	
		ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity	
		PC", Proceedings of the 11th Hotchips Conference, (8/17/1999),185-196	

EXAMINER

DATE CONSIDERED

PTO/SB/08A(08-03)
Approved for use through 07/31/2006, OMB 0651-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO	Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known		
INFORMATION DISCLOSURE	Application Number	10/676,887	
STATEMENT BY APPLICANT (Use as many sheets as necessary)	Filing Date	September 30, 2003	
,	First Named Inventor	Steven M. Bennett, et al.	
	Group Art Unit	2111	
	Examiner Name	Auve, Glenn A.	
Sheet 3 of 3	Attorney Docket No:	42P16348	

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
		SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615,(July 1999),43-48	
		SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001),	